



## Aparna Dey, DVCon U.S. 2021 General Chair

### 1. Can you give us a little background on DVCon?

Now in its 33<sup>rd</sup> year, DVCon has become the “must-attend” technical conference and exhibition for design and verification engineers, as well as IP integrators and chip architects. DVCon is sponsored by Accellera and attracts a number of sponsorships and exhibitors from both large and small companies in the industry. In addition to the U.S., there are DVCon conferences in Europe, China, and India that help to meet the needs of the local community and industry

### 2. With this being the first fully virtual DVCon in the U.S., what can attendees expect?

They can look forward to the same in-depth technical program that they are used to, but with more opportunity to attend more sessions. With the on-demand aspects afforded a virtual conference, they will not have to choose between sessions. The entire conference will be available for those that register for full-conference access. <https://2021.dvcon.org/registration>. We will also have some interactive, networking options during our virtual exhibits which I hope our attendees will enjoy.

### 3. Are there other options to register if someone does not need full-conference access?

We have a free option that includes access to our keynote, two panel sessions, a “Birds-of-a-feather” session on UVM and our exhibits.

### 4. What is your keynote topic this year?

This year’s keynote, “Computational Logistics for System and Software Verification,” will be given by Dr. Paul Cunningham, corporate vice president and general manager of the system verification group at Cadence. In his presentation, he will introduce the concept of verification throughput and highlight the significant opportunities we have as an industry to dramatically improve verification throughput on modern SoC designs.

### 5. What are some highlights of the DVCon U.S. program attendees should know about?

There will be two panel sessions that can be accessed for free: “Verification in the Open-Source Era,” and “Chip Design on Cloud - from Pipe Dream to Preeminence.” Both panels will offer attendees an opportunity to ask questions during a live Q&A. I think attendees will find them interesting and thought-provoking.

We also have approximately 15 informative poster sessions, four tutorials, 42 papers and 18 short workshops to choose from over the course of four days. Our short workshops have continued to grow in popularity since we introduced them a few years ago. They are offered to

create opportunities for smaller companies to be a part of the DVCon program and increase their visibility to the same level as the larger companies.

We currently have 20 exhibitors participating in our virtual expo. The expo will be interactive with many networking opportunities to give attendees the ability to meet online with peers and experts in the design and verification community.

There will be something for just about everyone at DVCon U.S. this year. We hope to see you online! <https://2021.dvcon.org/>